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Overview The ATmega8515 Is A Low-power CMOS 8-bit Microcontroller Based On The AVR Enhanced RISC Architecture. By Executing Powerful Instructions In A Single Clock Cycle, The ATmega8515 Achieves Throughputs Approaching 1 MIPS Per MHz Allowing The System Designer To Optimize Power Consumption Versus Processing Speed. Block Diagram Figure 2. Block Diagram INTERNAL OSCILLATOR WATCHDOG TIMER MCU ...

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